

AMENDMENTS TO THE CLAIMSLISTING OF CLAIMS

This listing of claims will replace all prior versions, and listings, of the claims in the application.

Claim 1. (Currently Amended) An electronic apparatus which is connected to another apparatus by a digital communication bus and performs transmission and reception of data through said digital communication bus, comprising:

a display unit; and

a control unit for controlling the operation of said display unit, wherein

said control unit detects whether said another apparatus and said digital communication bus are connected so as to form a loop or not, and when a result of said detection indicates that they are connected so as to form said loop, said control unit allows said display unit to perform a warning display, and

said control unit detects whether they are connected so as to form the loop or not by discriminating whether processes which are executed after a bus reset was generated have been finished within a predetermined period or not.

Claim 2. (Currently Amended). An electronic apparatus

according to claim 1, wherein each apparatus includes at least two bus ports.

Claim 3. (Original) An electronic apparatus according to claim 2, wherein when the processes which are executed after the reset was generated are not finished within the predetermined period, said control unit detects that they are connected so as to form said loop and allows said display unit to perform said warning display.

Claim 4. (Original) An electronic apparatus according to claim 2, wherein when the processes which are executed after the bus reset was generated are finished within the predetermined period, said control unit detects that they are not connected so as to form said loop and does not allow said display unit to perform said warning display.

Claim 5. (Original) An electronic apparatus according to claim 1, wherein said digital communication bus is an IEEE1394 serial bus.

Claim 6. (Currently Amended) A data communicating method whereby a plurality of electronic apparatuses are connected by a digital communication bus and transmission and reception of data

are performed through said digital communication bus, comprising the steps of:

detecting whether another apparatus among said plurality of electronic apparatuses and said digital communication bus are connected so as to form a loop or not in at least one of said plurality of electronic apparatuses; and

when it is detected that said digital communication bus is connected to said another apparatus so as to form said loop, allowing a warning display to be performed; and

detecting whether they are connected so as to form the loop or not by discriminating whether processes which are executed after a bus reset was generated have been finished within a predetermined period or not.

Claim 7. (Currently Amended) A data communicating method according to claim 6, wherein each of said plurality of apparatuses includes at least two bus ports.

Claim 8. (Original) A data communicating method according to claim 7, wherein when the processes which are executed after the bus reset was generated are not finished within the predetermined period, it is detected that they are connected so as to form said loop and said display unit is allowed to perform

said warning display.

Claim 9. (Original) A data communicating method according to claim 7, wherein when the processes which are executed after the bus reset was generated are finished within the predetermined period, it is detected that they are not connected so as to form said loop and said display unit is not allowed to perform said warning display.

Claim 10. (Original) A data communicating method according to claim 6, wherein said digital communication bus is an IEEE1394 serial bus.

Claim 11. (Currently Amended) An electronic apparatus which is connected to another apparatus by a digital communication bus and performs transmission and reception of data through said digital communication bus, comprising a control unit for controlling the operation of said apparatus, wherein

    said control unit detects whether said another apparatus and said digital communication bus are connected so as to form a loop or not, and when a result of said detection indicates that they are connected so as to form said loop, said control unit generates a control signal for allowing a warning display to be performed, and

said control unit detects whether they are connected so as to form said loop or not by discriminating whether processes which are executed after a bus reset was generated have been finished within a predetermined period or not.

Claim 12. (Currently Amended) An electronic apparatus according to claim 11, wherein each apparatus includes at least two bus ports.

Claim 13. (Original) An electronic apparatus according to claim 12, wherein when the processes which are executed after said bus reset was generated are not finished within the predetermined period, said control unit detects that they are connected so as to form said loop, and generates said control signal for allowing said warning display to be performed.

Claim 14. (Original) An electronic apparatus according to claim 12, wherein when the processes which are executed after the bus reset was generated are finished within the predetermined period, said control unit detects that they are not connected so as to form said loop and does not generate said control signal for allowing said warning display to be performed.

Claim 15. (Original) An electronic apparatus according to

claim 11, wherein said digital communication bus is an IEEE1394 serial bus.

Claim 16. (Currently Amended) A data processing method for an electronic apparatus which is connected to another electronic apparatus by a digital communication bus and performs transmission and reception of data through said digital communication bus, ~~wherein said control unit detects~~ comprising the steps of:

detecting whether said another apparatus and said digital communication bus are connected so as to form a loop or not, and when a result of said detection indicates that they are connected so as to form said loop, ~~said control unit generates a control signal for allowing a warning display to be performed;~~ and detecting whether they are connected so as to form the loop or not by discriminating whether processes which are executed after a bus reset was generated have been finished within a predetermined period or not.

Claim 17. (Currently Amended) A data processing method for an electronic apparatus according to claim 16, wherein each apparatus includes at least two bus ports.

Claim 18. (Original) A data processing method for an electronic apparatus according to claim 17, wherein when the

processes which are executed after the bus reset was generated are not finished within the predetermined period, it is detected that they are connected so as to form said loop and said control signal for allowing said warning display to be performed is generated.

Claim 19. (Original) A data processing method for an electronic apparatus according to claim 17, wherein when the processes which are executed after the bus reset was generated are finished within the predetermined period, it is detected that they are not connected so as to form said loop and said control signal for allowing said warning display to be performed is not generated.

Claim 20. (Original) A data processing method for an electronic apparatus according to claim 16, wherein said digital communication bus is an IEEE 1394 serial bus.